

FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

Applicant(s): Jonathan H. Fischer
Case: 41
Serial No.: 10/002,028
Filing Date: November 15, 2001
Group: 2828

U.S. PATENT DOCUMENTS

EXAMINER						FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE	

FOREIGN PATENT DOCUMENTS

EXAMINER						TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO

OTHER DOCUMENTS

EXAMINER			
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.	

- la 1. P.R. Gray et al., "Analysis and Design of Analog Integrated Circuits," Third Edition, John Wiley & Sons, Inc., pp. 227-228, 1993.
- la 2. Alan B. Grebene, "Bipolar and MOS Analog Integrated Circuit Design," John Wiley & Sons, Inc., pp. 215-216 and 278-280, 1984.

RECEIVED
OCT - 3 2003
TECHNOLOGY CENTER 2800

Examiner

Date Considered

la10/29/03

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.